

IN THE UNITED STATES PATENT OFFICE

Serial No.: 09/812,276

Filed: March 20, 2001

For: Semiconductor Device and Process of Production of Same

Inventor: Michio Horiuchi, et al.

Atty Doc. No.: 72-01

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In the interest of full disclosure, the following items are herewith identified in Form PTO-1449 and a copy of the same is hereby provided, for the convenience of the U.S. Patent and Trademark Office.

This Information Disclosure Statement is being filed before the mailing date of the first official action.

US PATENTS

(AA) USP 5,353,498, dated October 11, 1994.

(AB) USP 5,497,033, dated March 5, 1996.

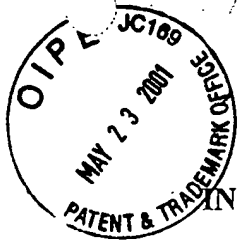
FOREIGN PATENTS

(AC) JP-A-7-7134, dated January 19, 1995. Relevance is a chip 14 is mounted on a base 10, with the active surface of the chip 14 facing the base 10, and is sealed with a molding material 24. The back surfaces of the molding material 24 and the chip 14 are then ground.

Respectfully submitted,

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PATENT

IN THE UNITED STATES PATENT OFFICE

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For: SEMICONDUCTOR DEVICE AND
PROCESS OF PRODUCTION OF
SAME

Inventor: Michio Horiuchi, et al.

Atty Doc. No.: 072-01

Certificate of Mailing

I hereby certify that this correspondence
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Post Office as first class mail postage
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John F. McNulty
John F. McNulty, Reg. No. 23,028
Dated: May 17, 2001

COVER LETTER WITH CERTIFICATE OF MAILING

Commissioner
for Patents
Washington, D.C. 20231

Sir:

Enclosed herewith are the following:

- (1) Cover letter with Certificate of Mailing;
- (2) Information Disclosure Statement (with attachments) and PTO form 1449
- (3) Paul & Paul postcard to be returned by the PTO.

THE COMMISSIONER IS HEREBY AUTHORIZED TO CHARGE ANY ADDITIONAL
FEES ASSOCIATED WITH THIS COMMUNICATION, OR CREDIT ANY
OVERPAYMENT, TO PAUL & PAUL DEPOSIT ACCOUNT NO. 16-0750, ORDER NO.

031).

Respectfully submitted,

John F. McNulty
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